

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A receiving terminal for a CDMA system, comprising:

a finger circuit having a plurality of finger circuit elements, each of said plurality of finger circuit elements for making a correlation between a received signal from a radio circuit connected to an antenna and a known signal, ~~and feeding out a plurality of outputs from the finger circuit elements as~~ and for outputting a correlated received signal as a result of the correlation; and

a rake circuit for combining the ~~plurality of outputs~~ correlated received signals output from the plurality of finger circuit elements;

wherein the rake circuit includes a level judgment circuit for executing electric field level judgment based on the correlated received ~~signal~~ signals output from the plurality of finger circuit elements and a predetermined threshold level; and

wherein an operation of at least one finger circuit element can be suspended for a fixed, predetermined time period according to the result of the electric field level judgment.

2. (Previously Presented) The receiving terminal for a CDMA system according to claim 1, wherein operation of a control clock supply to the at least one finger circuit element is suspended for power consumption reduction according to the result of the electric field level judgment by the level judgment circuit.

3. (Previously Presented) The receiving terminal for a CDMA system according to claim 1, wherein operation of a control clock supply to a timing circuit in the at least one finger circuit element is suspended according to the result of the electric field level judgment by the level judgment circuit.

4. (Previously Presented) The receiving terminal for a CDMA system according to claim 1, wherein operation of a control clock supply is suspended after the lapse of a predetermined period of time.
5. (Cancelled)
6. (Previously Presented) The receiving terminal for a CDMA system according to claim 1, wherein the predetermined threshold level is preset in a memory.
7. (Previously Presented) The receiving terminal for a CDMA system according to claim 6, wherein the memory is an E²PROM, and the predetermined threshold level therefrom is supplied under CPU control to the rake circuit.
8. (Currently Amended) The receiving terminal for a CDMA system according to claim 1, wherein each of the plurality of finger circuit elements makes a correlation between the received signal from the radio circuit and data of said known signal, demodulates the correlated data to symbol unit data, and feeds out the demodulated data to the rake circuit.
9. (Currently Amended) The receiving terminal for a CDMA system according to claim 1, wherein the rake circuit further includes a level measuring circuit for executing level measurement for ~~each output of the plurality of outputs~~ correlated received signal output from each corresponding finger circuit element of the plurality of finger circuit elements by computing the power level in a pilot symbol part of each slot in one frame of ~~each output of the plurality of outputs from the finger circuit elements~~ correlated received signal and adding together the results of the computation for the slots in the frame of ~~each output~~ correlated received signal.
10. (Currently Amended) The receiving terminal for a CDMA system according to claim 1, wherein the level judgment circuit obtains the differences between (1) a maximum level of electric field levels from among ~~said plurality of outputs~~ the correlated received signals output from the plurality of finger circuit elements and (2) an electric field level of each of

said ~~plurality of outputs~~ correlated received signals output from the plurality of finger circuit elements, and compares the differences with the predetermined threshold level.

11. (Previously Presented) A receiving terminal for a CDMA system for receiving received signals from a plurality of signal propagation channels, comprising:

a circuit for judging an electric field level of each of the received signals;

wherein operation of a control clock supply to a circuit system, which is making a correlation between a received signal that is judged to have a low electric field level and a known signal, is suspended for a fixed, predetermined period of time for power consumption reduction.

12. (Currently Amended) A receiver for a CDMA system, comprising:

a finger circuit having a plurality of finger circuit elements, each of said plurality of finger circuit elements for making a correlation between a received signal from a radio circuit connected to an antenna and a known signal, ~~and feeding out a plurality of outputs from the finger circuit elements as~~ and for outputting a correlated received signal as a result of the correlation; and

a rake circuit for combining the ~~plurality of outputs~~ correlated received signals output from the finger circuit elements;

wherein the rake circuit includes a level judgment circuit for executing electric field level judgment based on the correlated received ~~signal~~ signals output from the plurality of finger circuit elements and a predetermined threshold level; and

wherein an operation of at least one finger circuit element can be suspended for a fixed, predetermined time period according to the result of the electric field level judgment.

13. (Previously Presented) The receiver for a CDMA system according to claim 12, wherein operation of a control clock supply to the at least one finger circuit element is suspended for power consumption reduction according to the result of the electric field level judgment by the level judgment circuit.

14. (Previously Presented) The receiver for a CDMA system according to claim 12, wherein operation of a control clock supply to a timing circuit in the at least one finger circuit element is suspended according to the result of the electric field level judgment by the level judgment circuit.

15. (Previously Presented) The receiver for a CDMA system according to claim 12, wherein operation of a control clock supply is suspended after the lapse of a predetermined period of time.

16. (Cancelled)

17. (Previously Presented) The receiver for a CDMA system according to claim 12, wherein the threshold level is preset in a memory.

18. (Previously Presented) The receiver for a CDMA system according to claim 17, wherein the memory is an E²PROM, and the predetermined threshold level therefrom is supplied under CPU control to the rake circuit.

19. (Currently Amended) The receiver for a CDMA system according to claim 12, wherein each of the plurality of finger circuit elements makes a correlation between the received signal from the radio circuit and data of said known signal, demodulates the correlated data to symbol unit data, and feeds out the demodulated data to the rake circuit.

20. (Currently Amended) The receiver for a CDMA system according to claim 12, wherein the rake circuit further includes a level measuring circuit for executing level measurement for each output of the plurality of outputs correlated received signal output from each corresponding finger circuit element of the plurality of finger circuit elements by computing the power level in a pilot symbol part of each slot in one frame of each output of the plurality of outputs from the finger circuit elements correlated received signal and adding together the results of the computation for the slots in the frame of each output correlated received signal.

21. (Currently Amended) The receiver for a CDMA system according to claim 12, wherein the level judgment circuit obtains the differences between (1) a maximum level of electric field levels from among ~~said plurality of outputs~~ the correlated received signals output from the plurality of finger circuit elements and (2) an electric field level of each of ~~said plurality of outputs~~ correlated received signals output from the plurality of finger circuit elements, and compares the differences with the predetermined threshold level.
22. (Currently Amended) A receiver for a CDMA system for receiving received signals from a plurality of signal propagation channels, comprising:
a circuit for judging an electric field level of each of the received signals;
wherein ~~the~~ an operation of a control clock supply to a circuit system, which is making a correlation between a received signal that is judged to have a low electric field level and a known signal, is suspended for a fixed, predetermined period of time.
23. (Currently Amended) A receiving method for a CDMA system with step for making a correlation of a received signal and a known signal and combining a plurality of correlated signals for level measurement, the method further comprising:
executing electric field level judgment according to the correlated received signals and a predetermined threshold level, and suspending an operation of a ~~predetermined~~ circuit that performs correlation for a fixed, predetermined time period according to the result of the level judgment.
24. (Previously Presented) A receiving method for a CDMA system for receiving received signals from a plurality of signal propagation channels including the steps of:
judging an electric field level of the received signal from each signal propagation channel; and
suspending operation of a control clock supply to a circuit, which is making a correlation between a signal from a low electric field level signal propagation channel and a known signal, for a fixed, predetermined period of time.
25. (Currently Amended) The receiving terminal for a CDMA system according to claim 1,

wherein the rake circuit further includes:

a main synthesizer for combining electric field levels of all of the ~~plurality of outputs~~ correlated received signals output from the plurality of finger circuit elements; and

a sub-synthesizer for combining electric field levels of a subset of the ~~plurality of outputs~~ correlated received signals output from the plurality of finger circuit elements; and

wherein the level judgment circuit executes the electric field level judgment by comparing the predetermined threshold value with a difference between an output of the main synthesizer and an output of the sub-synthesizer.

26. (Currently Amended) The receiving terminal for a CDMA system according to claim 25,

wherein the subset of the ~~plurality of outputs~~ correlated received signals output from the plurality of finger circuit elements includes at least ~~an output of a correlated received signal output from~~ a finger circuit element that has a maximum electric field level among the ~~plurality of outputs~~ correlated received signals output from the plurality of finger circuit elements.

27. (Currently Amended) The receiving terminal for a CDMA system according to claim 25, wherein the rake circuit further includes:

a path level judging circuit for determining a particular ~~output~~ correlated received signal that has a maximum electric field level from among the ~~plurality of outputs~~ correlated received signals output from the plurality of finger circuit elements, and for comparing a second threshold value with differences between (1) the electric field level of the particular ~~output~~ correlated received signal and (2) an electric field level of each of the ~~outputs of the plurality of outputs~~ correlated received signals output from the plurality of finger circuit elements; and

wherein the subset of the ~~plurality of outputs~~ correlated received signals output from the plurality of finger circuit elements that are combined by the sub-synthesizer is determined based on results of the comparisons by the path level judging circuit.